Figure 1   Timing Loop Prevention   PRIOR ART
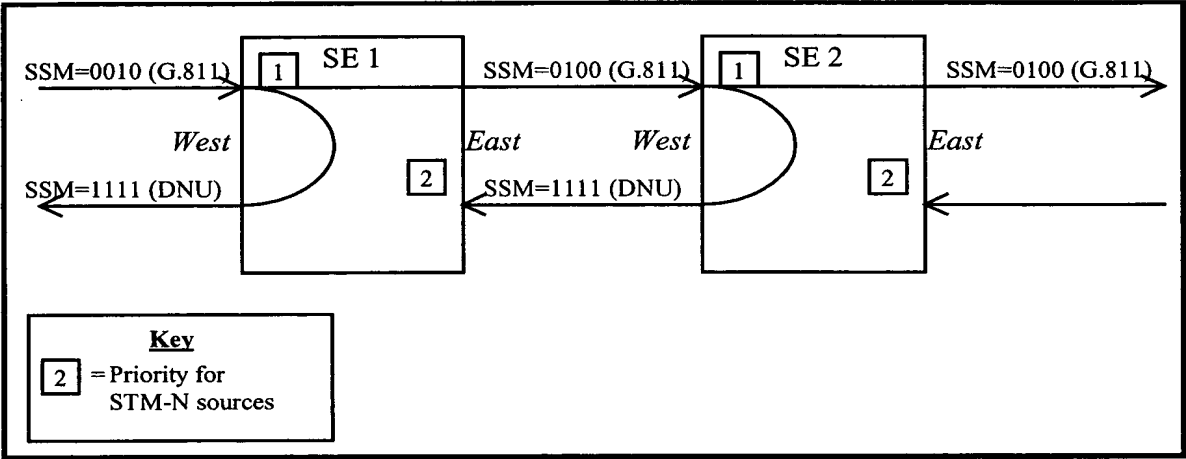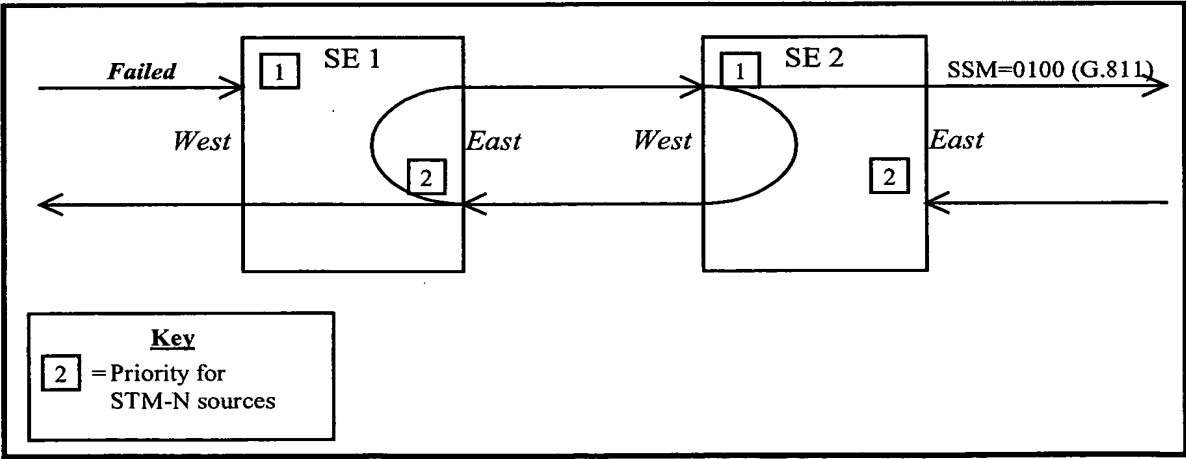

Figure 2   Synchronisation Timing Loop   PRIOR ART

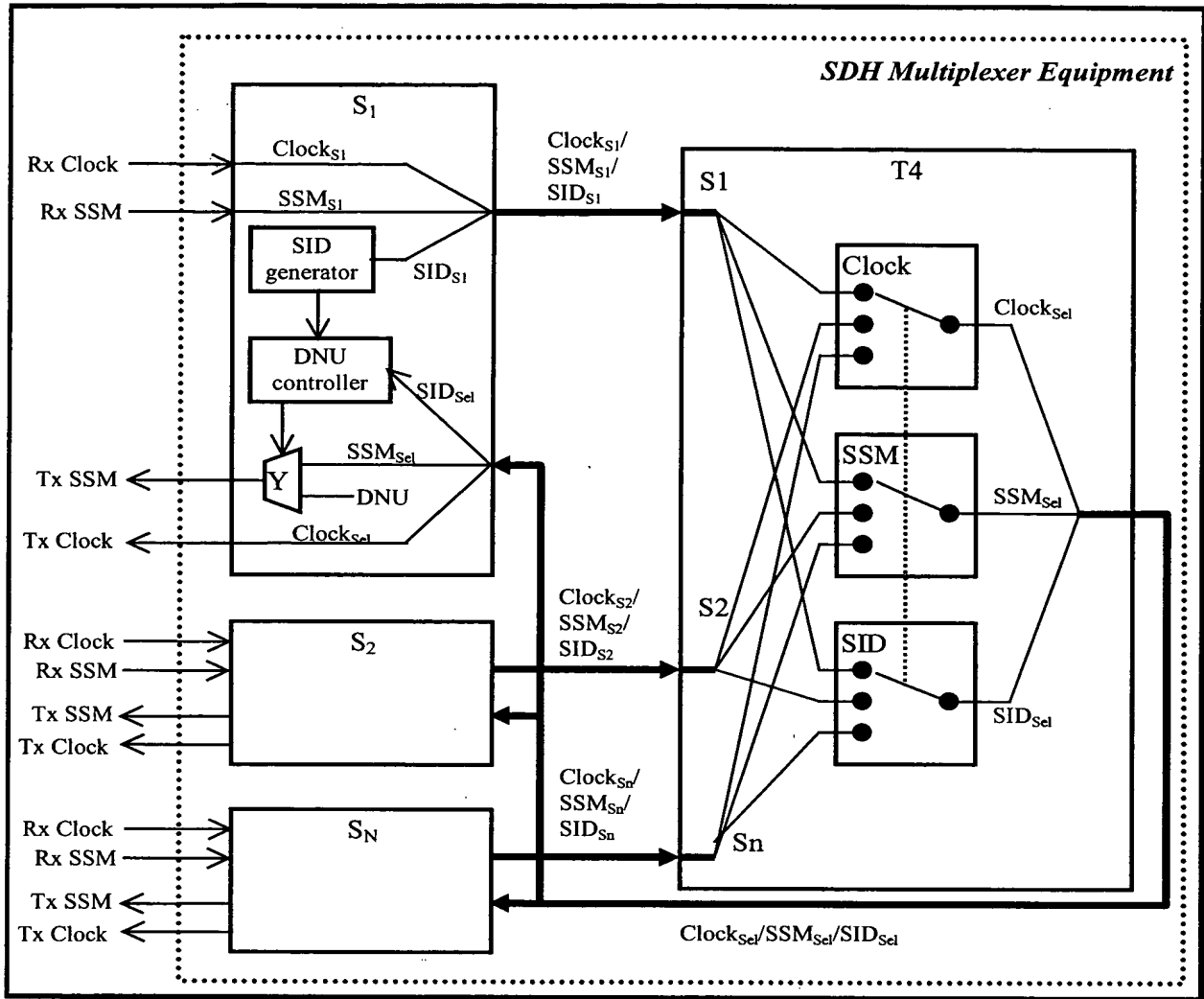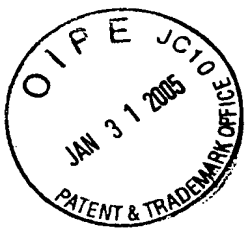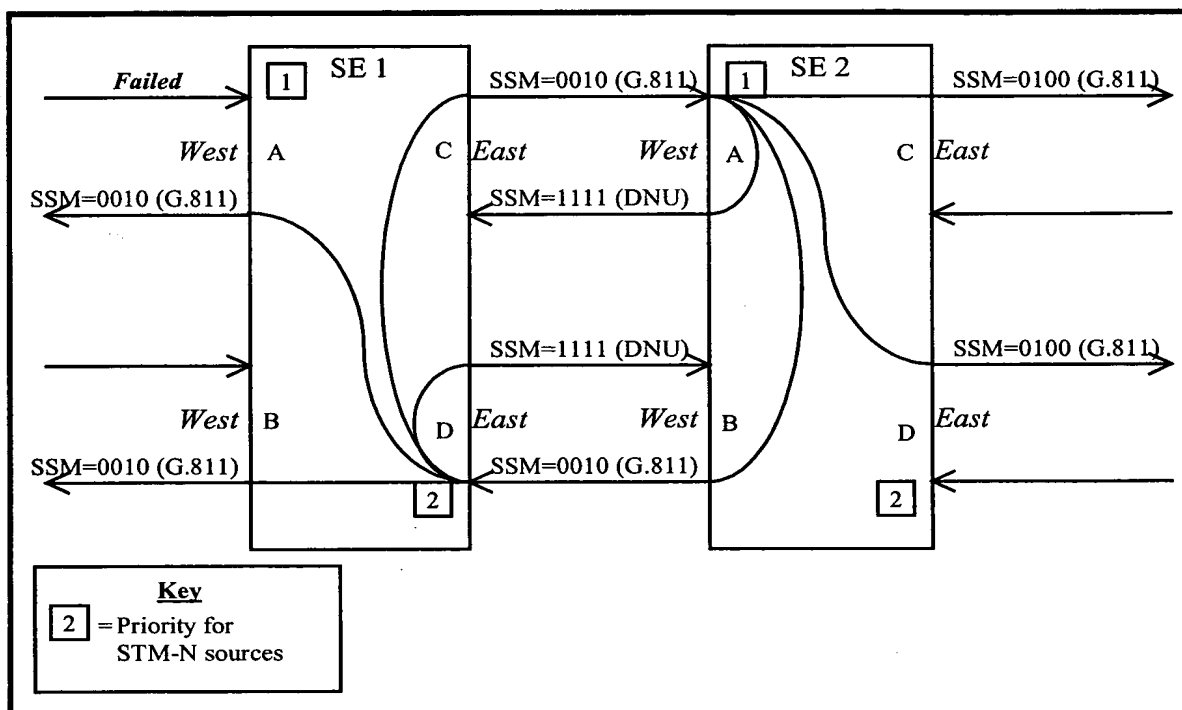Figure 3   General use of SID in Timing Loop Prevention   PRIOR ART

Replacement

Figure 4   Multiple Port Timing Loop   PRIOR ART

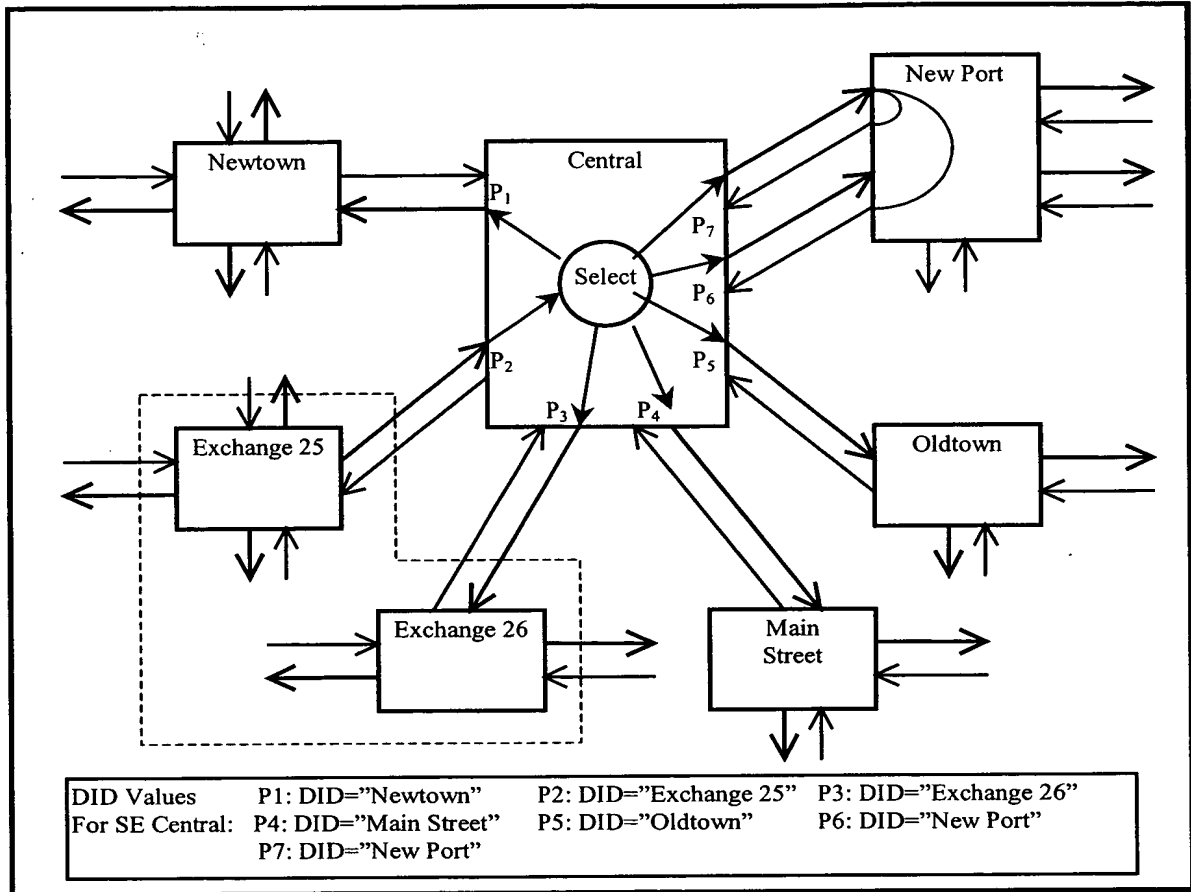Figure 5   Destination IDs